**Winter Internship Program 2012**

**Department Of Electronics**

**Leading Electronic Applications & Design Researchers Setup (LEADERS)**

**(17th December 2012 - 6th January 2013)**

**REGISTRATION FORM**

|  |  |  |  |
| --- | --- | --- | --- |
| Internee Information | | | |
| Name: |  | Roll Number: |  |
| E-Mail Address: |  | Contact no: |  |
| Address: |  |  |  |
|  | | | |
|  | | | |
| Area Of Interest | | | |

Area of Interest (Double Click any one checkbox of your interested field to switch between checked or unchecked):

Project Based Learning (PBL)  Microcontroller & Microprocessor

FPGAs  Digital Electronics

DSP

In case of PBL please give brief description of your project in the space provided below

[Edit Text here]

*N.B: Email filled forms at* [*info@es.muet.edu.pk*](mailto:info@es.muet.edu.pk)